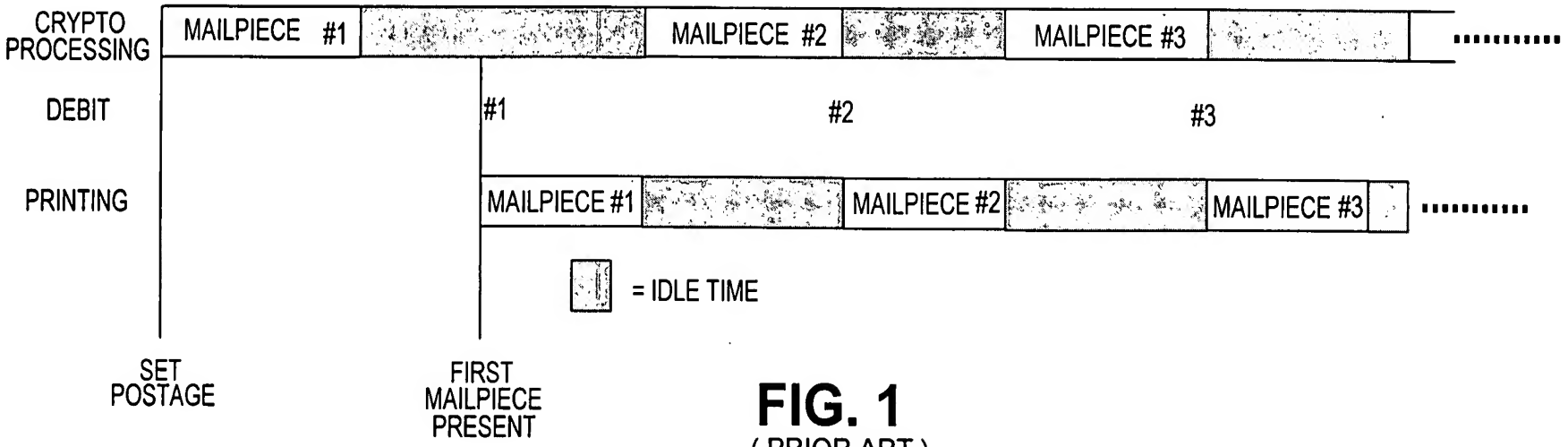

FIG. 1
(PRIOR ART)

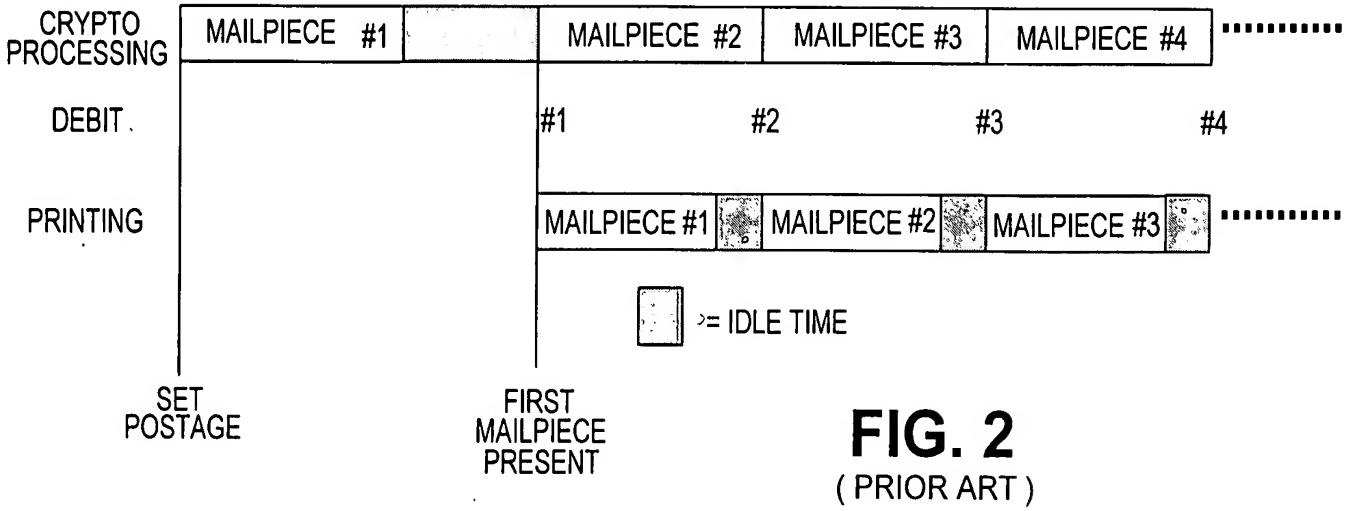

FIG. 2
(PRIOR ART)

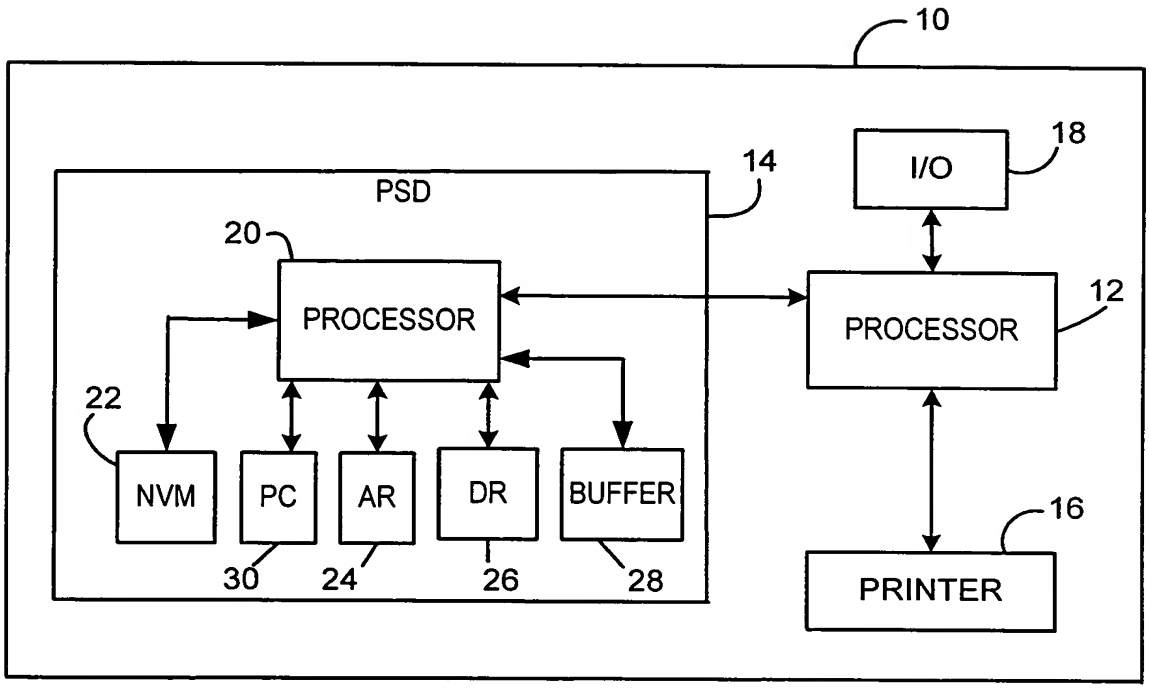
10
PSD
14
I/O
18
20
PROCESSOR
PROCESSOR
12
22
NVM
PC
AR
DR
BUFFER
16
PRINTER
30
24
26
28

FIG.3

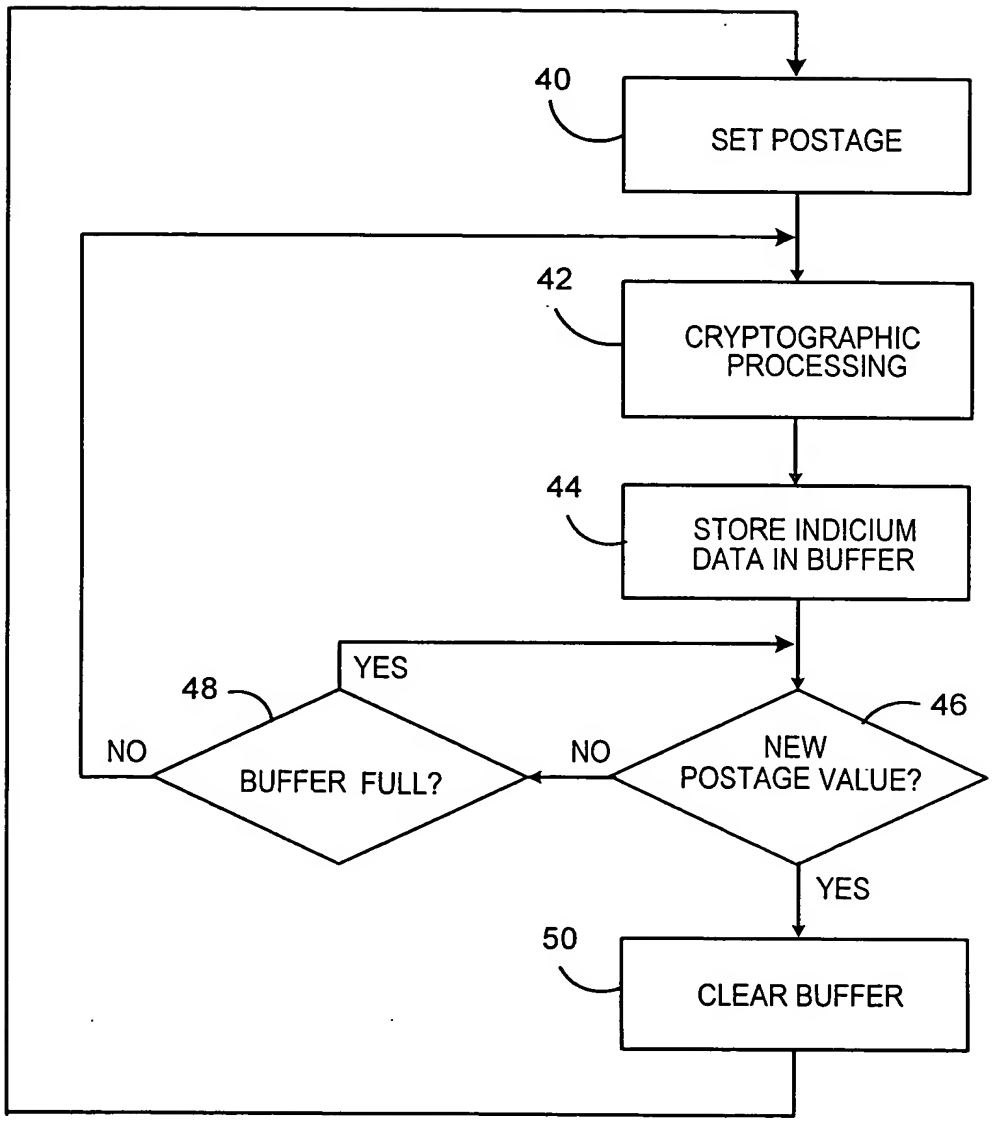
40
SET POSTAGE
42
CRYPTOGRAPHIC PROCESSING
44
STORE INDICIUM DATA IN BUFFER
46
NEW POSTAGE VALUE?
YES
NO
48
BUFFER FULL?
NO
YES
50
CLEAR BUFFER

FIG. 4

28 BUFFER	REGISTER VALUES ASSOCIATED WITH INDICIUM DATA PIECE COUNT X	ASCENDING REGISTER Y	DESCENDING REGISTER Z
INDICIUM DATA 1	X+1	Y + (AMOUNT)	Z - (AMOUNT)
INDICIUM DATA 2	X+2	Y + 2(AMOUNT)	Z - 2(AMOUNT)
INDICIUM DATA 3	X+3	Y + 3(AMOUNT)	Z - 3(AMOUNT)
INDICIUM DATA N-1	X+(N-1)	Y + (N-1)(AMOUNT)	Z - (N-1)(AMOUNT)
INDICIUM DATA N	X+N	Y + N(AMOUNT)	Z - N(AMOUNT)

FIG. 5

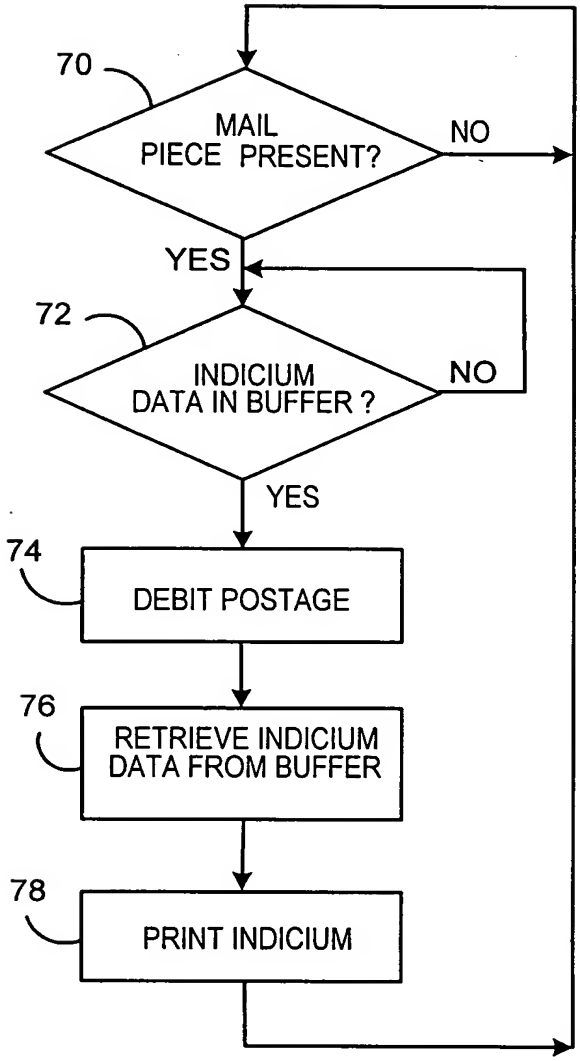
70
MAIL PIECE PRESENT?
NO
YES
72
INDICIUM DATA IN BUFFER ?
NO
YES
74
DEBIT POSTAGE
76
RETRIEVE INDICIUM DATA FROM BUFFER
78
PRINT INDICIUM

FIG. 6

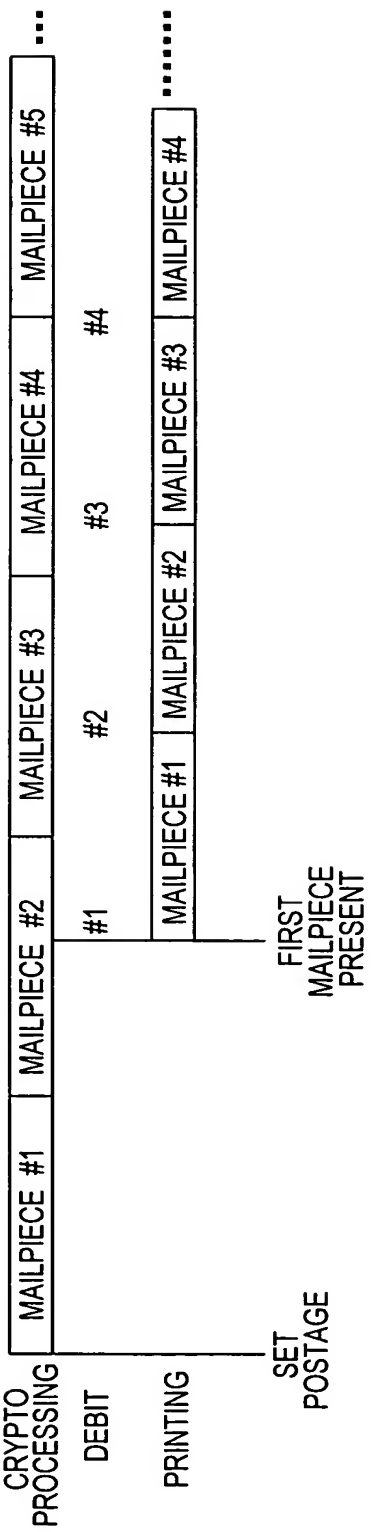
CRYPTO PROCESSING
DEBIT
PRINTING
SET POSTAGE
MAILPIECE #1
MAILPIECE #2
MAILPIECE #3
MAILPIECE #4
MAILPIECE #5
#1
#2
#3
#4
FIRST MAILPIECE PRESENT
MAILPIECE #1
MAILPIECE #2
MAILPIECE #3
MAILPIECE #4

FIG. 7

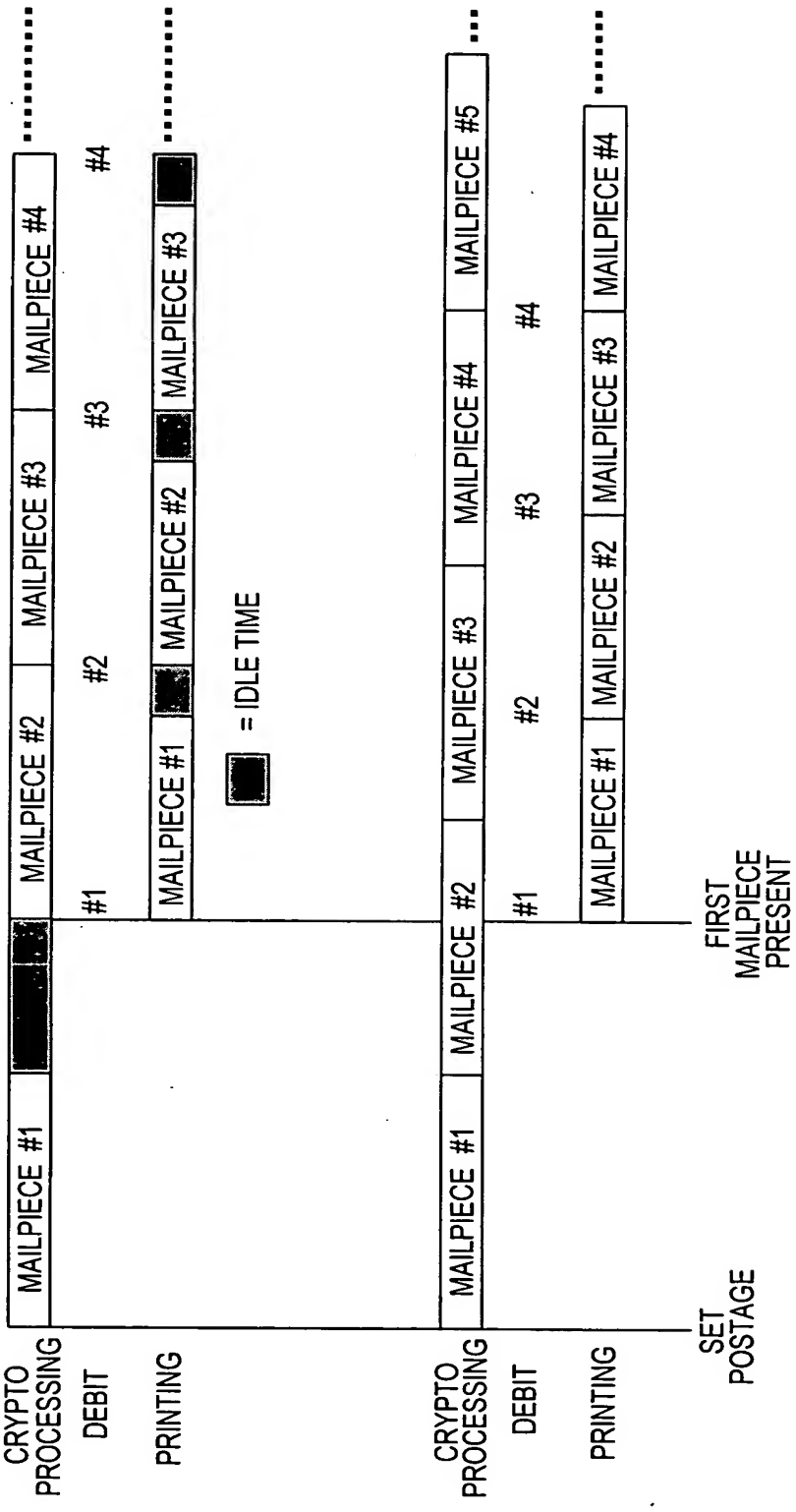
CRYPTO PROCESSING
DEBIT
PRINTING
SET POSTAGE
FIRST MAILPIECE PRESENT
MAILPIECE #1
MAILPIECE #2
MAILPIECE #3
MAILPIECE #4
MAILPIECE #5
#1
#2
#3
#4
= IDLE TIME

FIG. 8